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DESCRIPTIVE ABSTRACT

CDMA receiver with parallel interference suppression and optimized synchronization.

According to the invention, the synchronization means are placed in the final stage (ED) and are used both in the final stage (ED) and in the preceding interference suppression stages (ESI_i , ESI_1).

Application to radiocommunications with mobiles.

(Fig. 8)

ABSTRACT OF THE DISCLOSURE

A receiver for transmission and parallel interference has multiple access interference suppression stages having K channels, each comprising a correlation device corresponding to a particular pseudorandom sequence and interference generation and suppression device. Each stage delivers to the following stage signals at least partly freed from multiple access interferences. A decision stage receives the signals from the channels of the preceding suppression stage. Each decision stage has a correlation device corresponding to one of the pseudorandom sequences and decision device to deliver data. Devices for producing synchronization signals can control the interference suppression and the decision device. The devices producing the synchronization signals have components placed in the channels of the final stage. Further, the synchronization signals produced by these devices control the decision device of the channels of the final stage and the interference estimation device of the at least one interference suppression stages following appropriate time shifts.

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